

AMENDMENT UNDER 37 C.F.R. § 1.111
U.S. Application No.: 10/684,441

REMARKS

Claim 43 is herein cancelled without prejudice or disclaimer and claim 47 is added.

Claims 1 - 42 and 44 - 47 are presently pending.

Applicant thanks the Examiner for noting that claims 1 - 41 are allowable and that claims 44 - 46 possess allowable subject matter.

Claims 4, 5, 6, 11, 12, 16, 17, 18, 23, 24, 28, 29, 30, 35, 36, 37, 38, 39, 45 and 46 are herein amended to correct for typographical errors and/or to clarify the antecedent basis of a feature or features.

I. 35 U.S.C. § 102 Rejections

Claims 42 - 43 are rejected under 35 U.S.C. § 102 as being anticipated by Applicant's Figures 1 - 5 (labeled as prior art). Claim 42 is also rejected under 35 U.S.C. § 102 in view of US 6,525,949 to Johnson. For the following reasons these rejections are respectfully traversed.

Claim 43 is herein cancelled, obviating the rejection as to this claim.

Applicant herein amends subject matter noted by the Examiner as allowable (from claim 44) into independent claim 42. Independent claim 42 is therefore patentable, as are dependent claims 44 - 46 at least by virtue of their dependency.

New claim 47 is patentable for reciting subject matter from claims 45 - 46 noted by the Examiner as allowable.

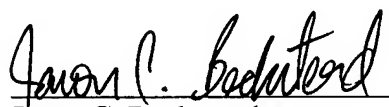
Because all claims are in condition for allowance, Applicant respectfully requests that this application be passed to issue.

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If any points remain which the Examiner feels may be resolved through a personal or telephone interview, the Examiner is invited to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,


Jason C. Beckstead
Registration No. 48,232

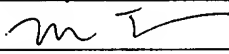
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Date: February 22, 2005

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Date:	<u>February 22, 2005</u>
Signed:	 Mariann Tam

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AMENDMENTS TO THE DRAWINGS

A replacement sheet for Fig. 4 (sheet 3/30 and inclusive of Fig. 3) is provided herewith in which a node is properly located at the first three-juncture intersection connecting the gate of transistor 202 to logic gate 207.

A replacement sheet for Fig. 5 (4/30) is provided herewith in which the reference numeral for the transistor for circuit 31 is changed from “323” to “311” so as to be consistent with the specification at page 7, line 26, and to correct for the inadvertent double labeling of a transistor 323 in both of circuits 31 and 32.

Attachment: Two (2) Replacement Sheets for Figs. 3 – 5.